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	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
	10/708,442	03/03/2004	Tien-Tzu CHEN	AITP0007USA	2441	
	27765	7590 01/21/2005		EXAMINER		
	(NAIPC) NO	(NAIPC) NORTH AMERICA INTERNATIONAL PATENT OFFICE			CUNNINGHAM, TERRY D	
	P.O. BOX 506 MERRIFIELD	P.O. BOX 506 MERRIFIELD, VA 22116		ART UNIT	PAPER NUMBER	
	,			2816		
				DATE MAILED: 01/21/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		10/708,442	CHEN ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Terry D. Cunningham	2816				
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
2a)□ 3)□	2a) This action is <b>FINAL</b> . 2b) This action is non-final.						
Dispositi	on of Claims	•					
5)□ 6)⊠ 7)□	4)  Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration.  5)  Claim(s) is/are allowed.  6)  Claim(s) 1-20 is/are rejected.  7)  Claim(s) is/are objected to.						
Applicati	on Papers						
<ul> <li>9) The specification is objected to by the Examiner.</li> <li>10) The drawing(s) filed on 30 March 2004 is/are: a) accepted or b) objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).</li> <li>11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.</li> </ul>							
Priority under 35 U.S.C. § 119							
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>							
Attachment	• •	-					
2)	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date 3/3/04.	4) Interview Summary ( Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te				

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1-20 are rejected under 35 U.S.C. § 112, first paragraph, as based on a disclosure which is not enabling. Circuitry corresponding to charging transistors 50in in Fig. 5 is deemed critical or essential to the practice of the invention, but is not included in the claim(s). An arrangement lacking this feature is not enabled by the disclosure since it cannot be understood from the specification how the circuit will operate without such. As seen, it would be impossible for the capacitors to charge without such circuitry. *In re Mayhew*, 527 F.2d 1229, 188 USPQ 356 (CCPA 1976).

Claims 1-20 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

It is not understood how the circuit can operate without the above-discussed critical feature.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Tsai (USPAP 2002/0130703).

With respect to claims 1-16, Tsai discloses, in Fig. 5, a circuit comprising: "a first clock signal (Vphi1 provided to the capacitor in 116)"; "a second clock signal (Vphi2 provided to the capacitor in 108)"; "a first capacitor (capacitor in 116)"; "a second capacitor (capacitor in 108)"; "a first former-stage clock signal (Vphi1 provided to 124)"; "a second former-stage clock signal (vphi2 provided 132)"; "a first former-stage capacitor (132)"; "a second former-stage capacitor (124)"; "a first switching circuit (128)"; "a second switching circuit (120)"; "a first current reversing circuit (CMOS arrangement connected to the gate of 134)"; and "a second current reversing circuit (CMOS arrangement in 108)", all connected and operating similarly as recited by Applicant. The reference to Tsai expressly discloses that that the clock signals are non-overlapping.

With respect to claims 17-20, clearly the above circuit to Tsai will provide the recited method.

### Prior Art .

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Reference is specifically made to the reference of Thorp et al.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742. The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC January 13, 2005 Terry D. Cunningha Primary Examiner Art Unit 2816